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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

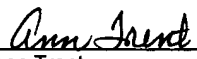
Applicant: Timothy A. Rost
Serial No: 10/785,648
Examiner: Long Pham
Filed: 02/24/2004
For: TRANSISTOR DESIGN AND LAYOUT FOR PERFORMANCE IMPROVEMENT WITH STRAIN

Docket No: TI-36595
Conf. No: 3709
Art Unit: 2814

ELECTION

Commissioner For Patents
P.O. Box 1450
Alexandria, VA 22313-1450

MAILING CERTIFICATE UNDER 37 C.F.R. § 1.8(a)
I hereby certify that the above correspondence is being deposited with the U.S. Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on 7-1-05.

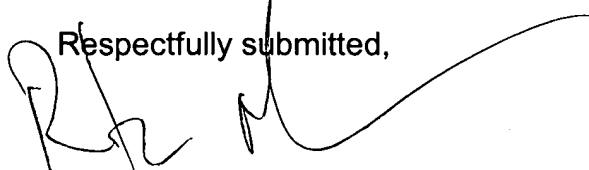

Ann Trent

Dear Sir:

This election is offered in response to the Examiner's restriction requirement mailed June 7, 2005.

Applicant hereby elects to pursue Species I, a method of making semiconductor devices, Claims 9-18, without traversing the Examiner's restriction requirement.

Respectfully submitted,


Peter K. McLarty
Attorney for Applicant
Reg. No. 44,923

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